



Network Processors: Architecture, Programming, and Implementation (Systems on Silicon)

Ran Giladi

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Network processors are the basic building blocks of today's high-speed, high-demand, quality-oriented communication networks. Designing and implementing network processors requires a new programming paradigm and an in-depth understanding of network processing requirements. This book leads the reader through the requirements and the underlying theory of networks, network processing, and network processors. It covers implementation of network processors and integrates EZchip Microcode Development Environment so that you can gain hands-on experience in writing high-speed networking applications. By the end of the book, the reader will be able to write and test applications on a simulated network processor.

- Comprehensive, theoretical, and practical coverage of networks and high-speed networking applications
- Describes contemporary core, metro, and access networks and their processing algorithms
- Covers network processor architectures and programming models, enabling readers to assess the optimal network processor type and configuration for their application
- Free download from <http://www.cse.bgu.ac.il/npbook> includes microcode development tools that provide hands-on experience with programming a network processor

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